



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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TECHNOLOGY CENTER 2800

Applicant(s): Stephen J. Battersby et al.
Serial No.: 09/728,189
For: LIQUID CRYSTAL DISPLAY AND METHOD OF
MANUFACTURE
Filed: December 01, 2000
Examiner: Akkapeddi, Prasad R.
Group Art Unit: 2871

Attorney Docket No.: PHB 34,424

Commissioner for Patents
Washington, D.C. 20231

AMENDMENT TRANSMITTAL FORM

Dear Sir:

XX Transmitted herewith is an Amendment Response in the above-identified application, a transmittal in duplicate and a potstcard.

 Petition for a one month extension of time pursuant to 37 C.F.R. §§ 1.136 and 1.137 is hereby made if, and to the extent, required. The fee for this extension of time is calculated to be \$ to extend the time for filing this response until .

The fee for any change in number of claims has been calculated as shown below.

CLAIMS AS AMENDED						
	Claims Remaining After Amendment		Highest Number Previously Paid	Present Extra	Rate	
Total Claims	13	Minus	20	0	0 X \$18.00	\$0.00
Independent Claims	3	Minus	3	0	0 X \$84.00	\$0.00
MULTIPLE DEPENDENT CLAIM FEE				x \$270.00 = \$		
TOTAL FEE FOR CLAIM CHANGES				\$0.00		

The total fee for this amendment is calculated to be \$.

 A check in the amount of \$ 0.00 is attached.

The Commissioner is hereby authorized to charge any additional fees under 37 C.F.R. §§1.16 and 1.17 which may be required with this communication or during the entire pendency of the application, or credit any overpayment, to **Deposit Account No. 01-0467**. A duplicate copy of this Form is enclosed.

January 24, 2003

Date

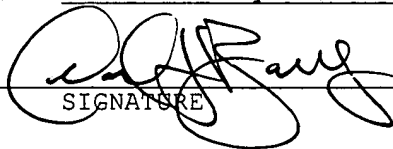


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CERTIFICATE OF MAILING

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING DEPOSITED WITH THE U.S. POSTAL SERVICE AS FIRST CLASS MAIL IN AN ENVELOPE ADDRESSED TO: COMMISSIONER FOR PATENTS, WASHINGTON, D.C. 20231, ON January 24, 2003.

DAVID L. BARNES
NAME


SIGNATURE

1/24/03.
DATE



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AMENDMENT

Dear Sir:

In response to the Office Action dated November 6, 2002,
please amend the above-noted application as follows:

IN THE CLAIMS

Please amend claims 1, 6, 10 and 11 as follows:

1. (Amended) A transistor substrate for a liquid crystal
display comprising:

a substrate;

a transistor over said substrate, said transistor having an
insulated-gate staggered structure with substantially coplanar
source and drain regions on said substrate, a gate region, and a
gate insulator lying between said gate region and said source
and drain regions; and